

Mod 13 Synchronous Counter Circuit Diagram

Roelof Maarten Marie Oberman

Digital Electronics—GATE, PSUS AND ES Examination Satish K Karna, Test Prep for Digital Electronics—GATE, PSUS AND ES Examination

Digital Principles and Logic Design Techniques Arijit Saha, Nilotpal Manna, 2013-03

Digital Design with Cpld Applications and VHDL (Book Only) Robert Dueck, 2004-06-08

Electronic Counters Roelof Maarten Marie Oberman, 1973 Text at the graduate level; valuable for designers of digital circuits in both universities and industry.

Fundamental of Digital Electronics And Microprocessors A.K.Chhabra, 2005 In the recent years there has been rapid advances in the field of Digital Electronics and Microprocessor. This book is intended to help students to keep pace with these latest developments. The Present book is revised version of earlier book 'Introduction to Digital Computers' by the same author. Now this book is written in a lucid and simple language, which gives clear explanation of basics of Digital Electronics, Computers and microprocessors.

Digital Electronics Dr. P. Kannan, Mrs. M. Saraswathy, 2018-10-01 This book is extensively designed for the third semester ECE students as per Anna university syllabus R-2013. The following chapters constitute the following units Chapter 1, 2 and :-Unit 1 Chapter 3 covers :-Unit 2 Chapter 4 and 5 covers:-Unit 3 Chapter 6 covers :- Unit 4 Chapter 7 covers :- Unit 5 Chapter 8 covers :- Unit 5 CHAPTER 1: Introduces the Number System, binary arithmetic and codes. CHAPTER 2: Deals with Boolean algebra, simplification using Boolean theorems, K-map method, Quine McCluskey method, logic gates, implementation of switching function using basic Logical Gates and Universal Gates. CHAPTER 3: Describes the combinational circuits like Adder, Subtractor, Multiplier, Divider, magnitude comparator, encoder, decoder, code converters, Multiplexer and Demultiplexer. CHAPTER 4: Describes with Latches, Flip-Flops, Registers and Counters CHAPTER 5: Concentrates on the Analysis as well as design of synchronous sequential circuits, Design of synchronous counters, sequence generator and Sequence detector CHAPTER 6: Concentrates the Design as well as Analysis of Fundamental Mode circuits, Pulse mode Circuits, Hazard Free Circuits, ASM Chart and Design of Asynchronous counters. CHAPTER 7: Discussion on memory devices which includes ROM, RAM, PLA, PAL, Sequential logic devices and ASIC. CHAPTER 8: Concentrate on the comparison, operation and characteristics of RTL, DTL, TTL, ECL and MOS families. We have taken enough care to present

the definitions and statements of basic laws and theorems, problems with simple steps to make the students familiar with the fundamentals of Digital Design.

Lab Manual Gregory L. Moss,1990-11

A Textbook of Digital Electronics RS Sedha,2008 While writing this treatise,I have constantly kept in mind the requirements of all the students regarding the latest as well as changing trend of their examinations.To make it really useful for the students,latest examination questions of various indian universities as well as other examinations bodies have been included.The Book has been written in easy style,with full details and illustrations.

Digital Electronics Dr. S.K. Bhattacharya, Balaka Biswas,2023-10-13 This book includes the following chapters 1.Number Systems and Codes 2. Logic Gates 3. Boolean algebra and logic simplification 4. Design of Combinational Logic Circuits 5. Arithmetic Circuits 6. Decoder, Encoder, Multiplexer, Demultiplexer 7. Sequential Circuit Design 8. Shift Registers 9. Counters 10. A/D and D/A Converters 11. Logic Family

Modern Digital Systems Design John Y. Cheung,Jon G. Bredeson,1990

DIGITAL DESIGN R. ANANDA NATARAJAN,2015-01-17 Primarily intended for undergraduate engineering students of Electronics and Communication, Electronics and Electrical, Electronics and Instrumentation, Computer Science and Information Technology, this book will also be useful for the students of BCA, B.Sc. (Electronics and CS), M.Sc. (Electronics and CS) and MCA. Digital Design is a student-friendly textbook for learning digital electronic fundamentals and digital circuit design. It is suitable for both traditional design of digital circuits and HDL based digital design. This well organised text gives a comprehensive view of Boolean logic, logic gates and combinational circuits, synchronous and asynchronous circuits, memory devices, semiconductor devices and PLDs, and HDL, VHDL and Verilog programming. Numerous solved examples are given right after conceptual discussion to provide better comprehension of the subject matter. VHDL programs along with simulation results are given for better understanding of VHDL programming. Key features Well labelled illustrations provide practical understanding of the concepts. GATE level MCQs with answers (along with detailed explanation wherever required) at the end of each chapter help students to prepare for competitive examinations. Short questions with answers and appropriate number of review questions at the end of each chapter are useful for the students to prepare for university exams and competitive exams. Separate chapters on VHDL and Verilog programming along with simulated results are included to enhance the programming skills of HDL.

Digital Circuits and Devices Roger Kersey,1985

Digital Devices and Systems with PLD Applications Michael A. Miller,1996-12 This introductory text lays the groundwork for students new to digital circuitry so they will understand basic and complex concepts. Topics are presented in a thorough, yet easy to read manner. Chapters include separate troubleshooting sections and design applications. Applications sidebars

throughout provide practical uses of devices, circuits, and/or concepts. Students are introduced to a generic computer system ?BABE? in Chapter 9 that illustrates how binary digital computers work, how to troubleshoot digital circuits and provides hands-on training. An up-to-date, detailed section on PLDs (Programmable Logic Devices) with applications is also included, as is in-text software. ALSO AVAILABLE Laboratory Manual, ISBN: 0-314-21393-7 INSTRUCTOR SUPPLEMENTS CALL CUSTOMER SUPPORT TO ORDER Instructor's Guide, ISBN: 0-314-23191-0 Transparency Masters, ISBN: 0-314-21394-5

Digital Design and Computer Organisation D. Nasib S. Gill, J.B. Dixit, 2008-12 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems. It summarizes the tools of logic design and their mathematical basis, along with in depth coverage of combinational and sequential circuits. The book includes an accompanying CD that includes the majority of circuits highlighted in the text, delivering you hands-on experience in the simulation and observation of circuit functionality. These circuits were designed and tested with a user-friendly Electronics Workbench package (Multisim Textbook Edition) that enables your progression from truth tables onward to more complex designs. This volume differs from traditional digital design texts by providing a complete design of an AC-based CPU, allowing you to apply digital design directly to computer architecture. The book makes minimal reference to electrical properties and is vendor independent, allowing emphasis on the general design principles.

Digital Fundamentals and Applications Dr. C.K. Chanda, Sumit Banerjee, 2011-12

Digital Logic Design Brian Holdsworth, Clive Woods, 2002-11-01 New, updated and expanded topics in the fourth edition include: EBCDIC, Grey code, practical applications of flip-flops, linear and shaft encoders, memory elements and FPGAs. The section on fault-finding has been expanded. A new chapter is dedicated to the interface between digital components and analog voltages. A highly accessible, comprehensive and fully up to date digital systems text A well known and respected text now revamped for current courses Part of the Newnes suite of texts for HND/1st year modules

Design and Testing of Reversible Logic Ashutosh Kumar Singh, Masahiro Fujita, Anand Mohan, 2019-07-29 The book compiles efficient design and test methodologies for the implementation of reversible logic circuits. The methodologies covered in the book are design approaches, test approaches, fault tolerance in reversible circuits and physical implementation techniques. The book also covers the challenges and the reversible logic circuits to meet these challenges stimulated during each stage of work cycle. The novel computing paradigms are being explored to serve as a basis for fast and low power computation.

FSM-based Digital Design using Verilog HDL Peter Minns, Ian Elliott, 2008-04-30 As digital circuit elements decrease in physical size, resulting in increasingly complex systems, a basic logic model that can be used in the control and design of a range of semiconductor devices is vital. Finite State Machines (FSM) have numerous advantages; they can be applied to many areas (including motor control, and signal and serial data identification to name a few) and they use less logic than

their alternatives, leading to the development of faster digital hardware systems. This clear and logical book presents a range of novel techniques for the rapid and reliable design of digital systems using FSMs, detailing exactly how and where they can be implemented. With a practical approach, it covers synchronous and asynchronous FSMs in the design of both simple and complex systems, and Petri-Net design techniques for sequential/parallel control systems. Chapters on Hardware Description Language cover the widely-used and powerful Verilog HDL in sufficient detail to facilitate the description and verification of FSMs, and FSM based systems, at both the gate and behavioural levels. Throughout, the text incorporates many real-world examples that demonstrate designs such as data acquisition, a memory tester, and passive serial data monitoring and detection, among others. A useful accompanying CD offers working Verilog software tools for the capture and simulation of design solutions. With a linear programmed learning format, this book works as a concise guide for the practising digital designer. This book will also be of importance to senior students and postgraduates of electronic engineering, who require design skills for the embedded systems market.

Digital Electronics Rishabh Anand, The book covers the complete syllabus of subject as suggested by most of the universities in India. Proper balance between mathematical details and qualitative discussion. Subject matter in each chapter develops systematically from inceptions. Large number of carefully selected worked examples in sufficient details. Each chapter of the book is saturated with much needed test supported by neat and self-explanatory diagrams to make the subject self-speaking to a great extent. No other reference is required. Ideally suited for self-study.

High-level Synthesis Michael Fingeroff, 2010 Are you an RTL or system designer that is currently using, moving, or planning to move to an HLS design environment? Finally, a comprehensive guide for designing hardware using C++ is here. Michael Fingeroff's High-Level Synthesis Blue Book presents the most effective C++ synthesis coding style for achieving high quality RTL. Master a totally new design methodology for coding increasingly complex designs! This book provides a step-by-step approach to using C++ as a hardware design language, including an introduction to the basics of HLS using concepts familiar to RTL designers. Each chapter provides easy-to-understand C++ examples, along with hardware and timing diagrams where appropriate. The book progresses from simple concepts such as sequential logic design to more complicated topics such as memory architecture and hierarchical sub-system design. Later chapters bring together many of the earlier HLS design concepts through their application in simplified design examples. These examples illustrate the fundamental principles behind C++ hardware design, which will translate to much larger designs. Although this book focuses primarily on C and C++ to present the basics of C++ synthesis, all of the concepts are equally applicable to SystemC when describing the core algorithmic part of a design. On completion of this book, readers should be well on their way to becoming experts in high-level synthesis.

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